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PATENT  
8040-1022

**IN THE U.S. PATENT AND TRADEMARK OFFICE**

In re application of:

Hiroataka NAKANO et al.

Confirmation No. 9420

Serial No. 09/855,723

GROUP 2825

Filed May 16, 2001

Examiner Magid Y. Dimyan

SYSTEM OF MANUFACTURING SEMICONDUCTOR  
INTEGRATED CIRCUIT BY HAVING A CLIENT  
CONNECTED TO A MANUFACTURER VIA TWO-WAY  
COMMUNICATION

**AMENDMENT AFTER FINAL REJECTION**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

March 16, 2005

Sir:

Responsive to the Official Action of January 18, 2005,  
please amend the above-identified application as follows:

**Amendments to the Claims** are reflected in the listing  
of claims that begins on page 2 of this paper..

**Remarks** begin on page 13 of this paper.